

信昌電子陶瓷股份有限公司
Prosperity Dielectrics Co., Ltd.

No.566-1, Gaoshi Rd., Lujhu, Yangmei Dist., Taoyuan City 32668, Taiwan, R.O.C.
Tel. : +886-3-4753355 Fax : +886-3-4854959

SPECIFICATION FOR APPROVAL

DATE :

CUSTOMER : _____

PART NAME : _____ Thick Film Anti-Surge Chip Resistor

CUSTOMER'S DWG. NO. : _____

CUSTOMER'S PART NO. : _____

PDC PART NO. : **FNF SERIES APPROVED** _____

DESCRIPTION. : _____

ACTION	"V"	CUSTOMER'S SIGNATURE	NOTE
RESULT			
FULL APPROVED			
CONDITIONAL APPROVED			
REJECTED			

OUR ACTION	SIGNATURE
PREPARED By	<i>Jenny Tseng</i>
CHECKED By	<i>Tony Chou</i>
APPROVED By	<i>Byron Tsai</i>

CUSTOMER SIGNATURE FOR ACCEPTANCE

Contents.

Descriptions	Pages
01. Features	2
02. Applications	2
03. Dimension and construction	2
04. Power Derating Curve	3
05. Rating	3
06. Part Number	4
07. Marking / Soldering / Surge	4~5
08. Reliability Performance	6-8
09. Packing Information	9
10. Storage / Handling	10

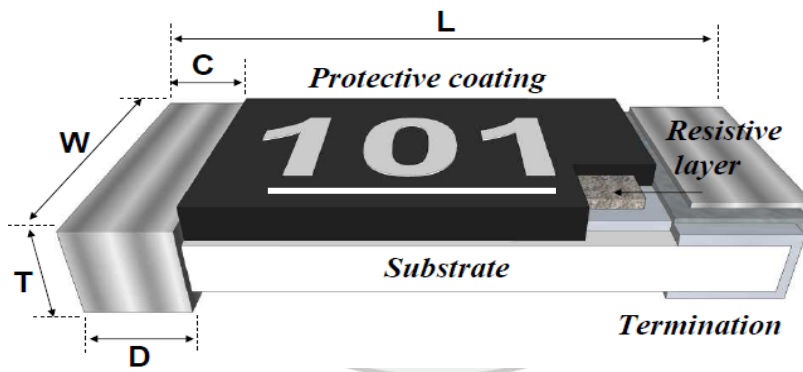
1. Features

- High reliability and compact size.
- Suitable for withstanding surge voltage.
- Suitable for lead free soldering.
- RoHS compliant & Halogen Free.
- Meet AEC-Q200

2. Applications

- Power supply.
- Automotive industry.
- Digital meter, Consumer electronics, M/B.
- LED Lighting.
- Industry control board.

3. Dimension and Construction

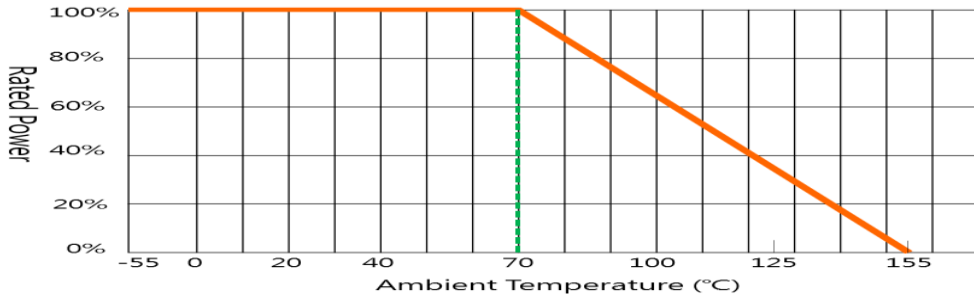


Unit : mm

Type	L	W	C	D	T
FNF03	1.60±0.10	0.80±0.10	0.30±0.20	0.30±0.20	0.45±0.10
FNF05	2.00±0.10	1.25±0.10	0.40±0.20	0.40±0.20	0.50±0.10
FNF06	3.10±0.10	1.60±0.10	0.50±0.25	0.50±0.25	0.55±0.10
FNF12	3.10±0.10	2.60±0.10	0.50±0.25	0.50±0.25	0.55±0.10
FNF20	5.00±0.20	2.50±0.20	0.60±0.25	0.60±0.25	0.60±0.10
FNF25	6.40±0.20	3.20±0.20	0.60±0.25	0.90±0.25	0.60±0.15

4. Power Derating Curve

Operating Temperature Range: -55 to +155 deg.C



5. Rating

Standard

Type	Size	Power Rating at 70°C	Max. RCWV	Max. Overload Voltage	Resistance Tolerance	Temperature Coefficient (ppm/°C)	Resistance Range		Standard Resistance Values
							Min.	Max.	
FNF03	0603	1/10W	50V	100V	±5%(J) ±10%(K) ±15%(L) ±20%(M)	±100	1Ω	1MΩ	E24
FNF05	0805	1/8W	150V	300V					
FNF06	1206	1/4W	200V	400V					
FNF12	1210	1/3W	200V	400V					
FNF20	2010	3/4W	200V	400V					
FNF25	2512	1W	200V	400V					

Power Enhance

Type	Size	Power Rating at 70°C	Max. RCWV	Max. Overload Voltage	Resistance Tolerance	Temperature Coefficient (ppm/°C)	Resistance Range		Standard Resistance Values
							Min.	Max.	
FNF05	0805	1/4W	150V	300V	±5%(J)	±100	1Ω	1MΩ	E24
FNF06	1206	1/3W	200V	400V	±10%(K)				
					±15%(L)				
					±20%(M)				

Application Note : $RCWV = \sqrt{P \times R}$ or Max. RCWV listed above, whichever is lower.

RCWV : Working Voltage (V) · P : Rated Power (W) · R : Resistance Value (Ω)

Solder-pad and trace size influences should be evaluated, and board surface temperature should keep not exceed 105°C when full rated power applied.

6.Part Number

Type	Size	Tolerance	Packing	Watt	R Code (GM)	Special Code
FNF	03 :0603	J :±5%	Paper Tape :	∅ :	XXX	" Null " :
	05 :0805	K :±10%	0603.0805	Standard		Standard
	06 :1206	L :±15%	1206.1210		3 digits	
	12 :1210	M :±20%	T : 5Kpcs	P :		-M :
	20 :2010		V : 10Kpcs	Power Enhance		Meet
	25 :2512		W : 20Kpcs			AEC-Q200
			Plastic Tape :			
			2010.2512			
			P : 4Kpcs			
			X : 8Kpcs			
			Y : 16Kpcs			

Example :

FNF25JP-104-M

→2512 size, tolerance 5%, plastic tape, 1W, 1 MΩ, Aec-Q200.

FNF06KT-104

→1206 size, tolerance 10%, paper tape, 1/4W, 100 KΩ, standard.

7.Marking/Soldering/Surge

Resistance value identify :

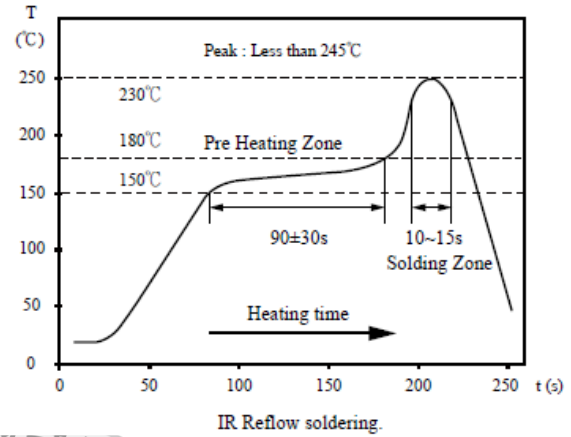
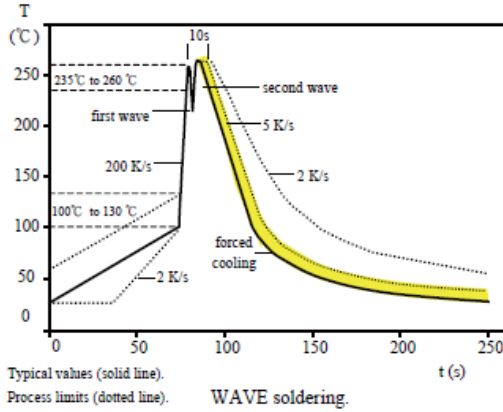
E24 ±5% : 3 Digits marking with underline to identify the resistance value

0603/0805/1206/1210/2010/2512



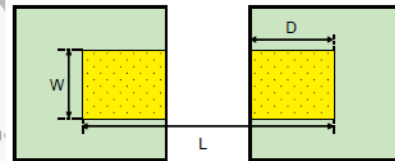
301 → $30 \times 10^1 = 300 \Omega$

Soldering Reference :



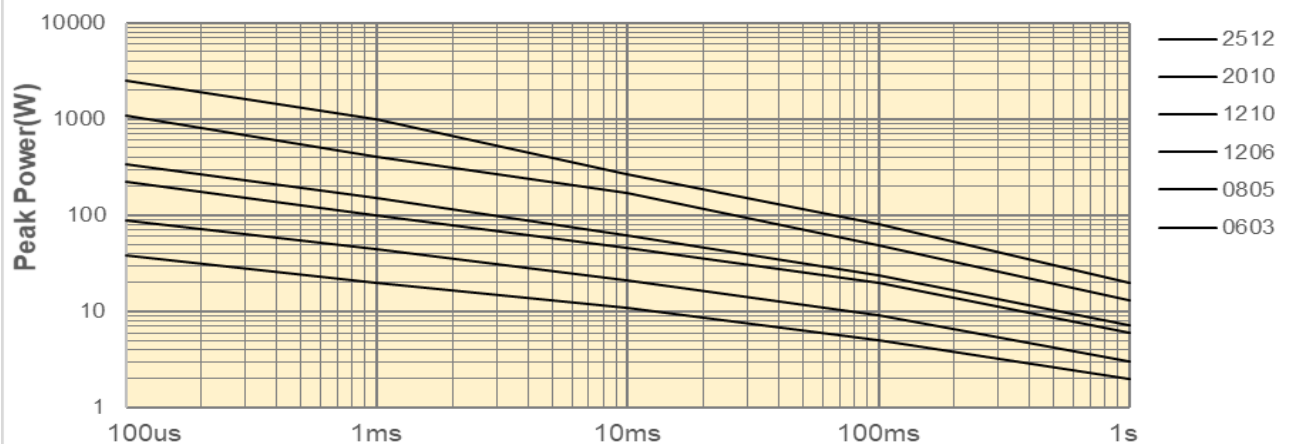
Recommend Solder Pad Dimensions :

Type	W	D	L
FNF03	0.90	1.00	3.00
FNF05	1.30	1.15	3.50
FNF06	1.80	1.30	4.70
FNF12	3.00	1.30	4.70
FNF20	3.00	1.50	6.80
FNF25	3.70	1.60	7.60



Unit:mm

FNF Single Pulse(Surge) Performance



8. Reliability Performance

8.1 AEC-Q200 Grade

Test Item	Specification	Test Method (AEC-Q200. IEC 60115)
DC Resistance	J : ± 5% K : ±10% L : ±15% M : ±20%	AEC-Q200 TABLE 7.1 IEC 60115-1 / JIS C 5201-1 , Clause 4.5 Measure the resistance Value.
High Temperature Exposure (Storage)	$\Delta R \leq \pm(3\%+0.1\Omega)$	AEC-Q200 TABLE 7.3 1000 hrs. @ T=125°C. Unpowered. Measurement at 24 ±2 hours after test conclusion.
Temperature Cycling	$\Delta R \leq \pm(1\%+0.1\Omega)$ No mechanical damage.	AEC-Q200 TABLE 7.4 1000 Cycles (-55°C to +125°C). Measurement at 24±2 hours after test conclusion.
Moisture Resistance	$\Delta R \leq \pm(1\%+0.1\Omega)$	AEC-Q200 TABLE 7.6 Test 65°C/80~100%RH/10Cycles. Measurement at 24±2 hours after test conclusion. (t=24hrs/cycle).
Biased Humidity	$\Delta R \leq \pm(5\%+0.1\Omega)$	AEC-Q200 TABLE 7.7 1000 hours 85°C/85%RH. 10% of operating power. Measurement at 24 ±2 hours after test conclusion.
Operational Life	$\Delta R \leq \pm(5\%+0.1\Omega)$	AEC-Q200 TABLE 7.8 Test 1000hr @ TA=125°C at specified rated power. Measurement at 24±2 hours after test conclusion.
External Visual	No visual damage and refer PDC marking code.	AEC-Q200 TABLE 7.9 Inspect device construction, marking and workmanship.
Physical Dimension	Within the spec.	AEC-Q200 TABLE 7.10 Verify physical dimensions to the applicable device detail specification.
Mechanical Shock	Within product specification tolerance and no visible damage.	AEC-Q200 TABLE 7.13 Test Peak value:100g's,Wave:Hail-sine, Duration:6ms,Velocity:12.3ft/sec.

FNF-M series. (AEC-Q200)
Anti-Surge
Thick-film Lead Free Chip Resistors

Test Item	Specification	Test Method (AEC-Q200. IEC 60115)
Vibration	No mechanical damage.	AEC-Q200 TABLE 7.14 5 g's for 20 min., 12 cycles each of 3 orientations. Test from 10-2000 Hz.
Resistance to Solder Heat	$\Delta R \leq \pm(1\% + 0.1\Omega)$ No mechanical damage.	AEC-Q200 TABLE 7.15 Solder dipping @ 270°C±5°C for 10sec.±1sec.
Thermal Shock	$\Delta R \leq \pm(1\% + 0.1\Omega)$ No mechanical damage.	AEC-Q200 TABLE 7.16 -55 to 155°C/ dwell time 15min/ Max transfer time 20sec/ 300cycles.
ESD	$\Delta R \leq \pm(3\% + 0.1\Omega)$	AEC-Q200-002 Test contact min. 1KV.
Solder Ability	Over 95% of termination must be covered with solder.	AEC-Q200 TABLE 7.18 a) Baking 155°C 4H, dipping 235°C 5s b) Steam 1H, dipping 215°C 5s c) Steam 1H, dipping 260°C 7s
Flammability	Refer UL-94.	AEC-Q200 TABLE 7.20 UL-94 V-0 or V-1 are acceptable
Board Flex	$\Delta R \leq \pm(1\% + 0.1\Omega)$ No mechanical damage.	AEC-Q200 TABLE 7.21 Bending 2mm 2512.2010.1210.1206, 3mm 0805.0603.
Terminal Strength	No mechanical damage	AEC-Q200 TABLE 7.22 Force 1 Kg for 60 seconds.

8.2 Standard Grade

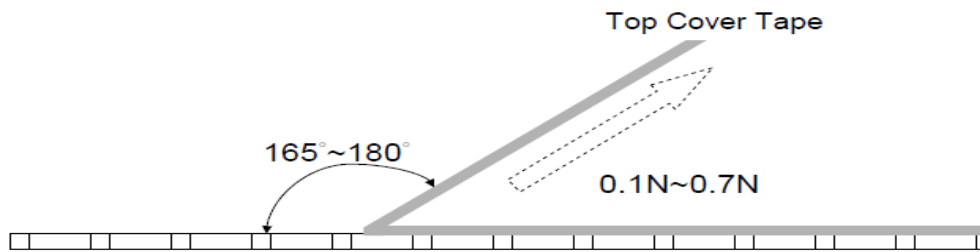
Test Item	Specification	Test Method
DC Resistance	J : ± 5% K : ±10% L : ±15% M : ±20%	IEC 60115-1 / JIS C 5201-1 , Clause 4.5 Measure the resistance Value.
Resistance to Solder Heat	$\Delta R \leq \pm(1\% + 0.1\Omega)$ No mechanical damage.	IEC 60115-1, Clause 4.18 Solder dipping @ 260°C±5°C for 10sec.±1sec.
Solder Ability	Over 95% of termination must be covered with solder.	IEC 60115-1, Clause 4.17 After immersing flux, dip in the 245±2°C molten solder bath for 3±0.5 sec.
Board Flex	$\Delta R \leq \pm(1\% + 0.1\Omega)$ No mechanical damage.	IEC 60115-1, Clause 4.33 Resistance change after bended on the 90mm PCB. Bending 2mm
Short Time Overload	$\Delta R \leq \pm(2\% + 0.1\Omega)$	IEC 60115-1, Clause 4.13 2.5 × Rated voltage for 5 seconds
Temperature Cycle	$\Delta R \leq \pm(1\% + 0.05\Omega)$ No mechanical damage.	IEC 60115-1, Clause 4.19 Repeat 5 cycles as follow. -55°C(30min)→25°C(2~3min)→155°C(30min) →25°C(2~3min)
Load Life Humidity	$\Delta R \leq \pm(3\% + 0.1\Omega)$	IEC 60115-1, Clause 4.24 40±2°C with relative humidity 90% ~ 95%, rated voltage for 1.5 hours ON 30 minutes OFF. Cycle repeated 1000 hours.
Temperature Coefficient of Resistance (TCR)	Within the spec.	IEC 60115-1, Clause 4.8 $T_1 \quad T_2$ Test temperature : 25°C~ +155°C $TCR(ppm/^\circ C) = (R_2 - R_1) / R_1 \times 1 / (T_2 - T_1) \times 10^6$
Load Life	$\Delta R \leq \pm(3\% + 0.1\Omega)$	IEC 60115-1, Clause 4.25 Rated voltage for 1.5 hours for followed by a pause 0.5 hour at 70±2°C. Cycle repeated 1000 hours.
Insulation Resistance	Between termination and coating must over 1000MΩ	IEC 60115-1, Clause 4.6 Test voltage : 100±15V

9. PACKAGING

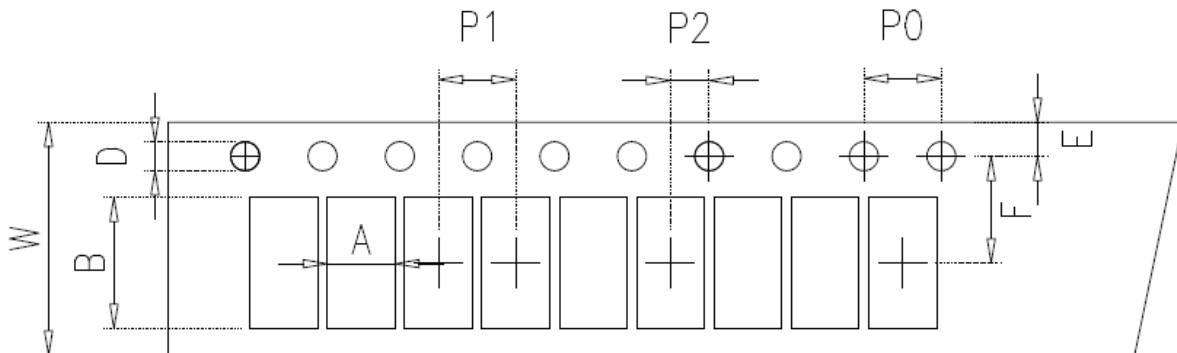
9.1 Peel Strength of Top Cover Tape

The peel speed shall be about 300 mm/min

The peel force of top cover tape shall between 0.1 to 0.7N

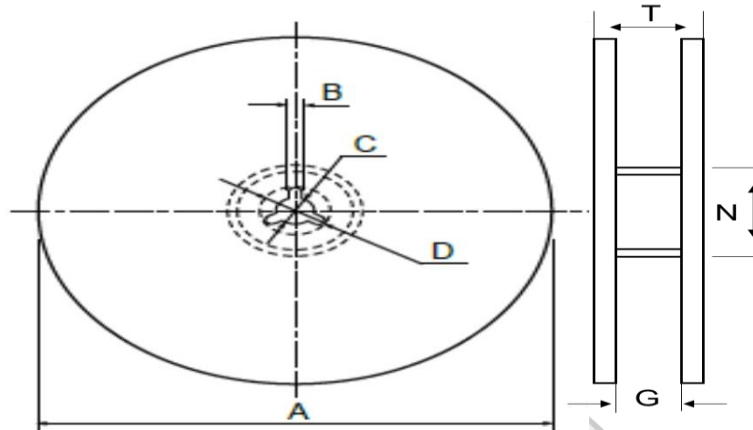


9.2 Tape Packaging Dimensions



Size	A	B	W	F	E	P1	P2	P0	D	unit:mm
0603	1.10±0.20	1.90±0.20	8.00±0.30	3.50±0.05	1.75±0.10	4.00±0.10	2.00±0.05	4.00±0.10	1.50+0.10/-0	
0805	1.65±0.20	2.40±0.20	8.00±0.30	3.50±0.05	1.75±0.10	4.00±0.10	2.00±0.05	4.00±0.10	1.50+0.10/-0	
1206	2.00±0.20	3.60±0.20	8.00±0.30	3.50±0.05	1.75±0.10	4.00±0.10	2.00±0.05	4.00±0.10	1.50+0.10/-0	
1210	3.00±0.20	3.60±0.20	8.00±0.30	3.50±0.05	1.75±0.10	4.00±0.10	2.00±0.05	4.00±0.10	1.50+0.10/-0	
2010	2.80±0.20	5.50±0.20	12.00±0.30	5.50±0.05	1.75±0.10	4.00±0.10	2.00±0.05	4.00±0.10	1.50+0.10/-0	
2512	3.50±0.20	6.70±0.20	12.00±0.30	5.50±0.05	1.75±0.10	4.00±0.10	2.00±0.05	4.00±0.10	1.50+0.10/-0	

9.3 Reel Dimensions



Size	Packaging Q'ty	A	N	C	D	B	G	T
0603	5kpcs/Reel	178.0±2.0	60.0±0.5	13.0±0.5	20(Min.)	2.0±0.5	10.0±1.5	14.9max.
0805	10kpcs/Reel	254.0±2.0	100.0±1.0	13.5±0.5	20(Min.)	2.0±0.5	10.0±1.5	14.9max.
1206	20kpcs/Reel	330.0±2.0	100.0±1.0	13.5±0.5	20(Min.)	2.0±0.5	10.0±1.5	14.9max.
2010	4kpcs/Reel	178.0±2.0	60.0±0.5	13.0±0.5	20(Min.)	2.0±0.5	13.8±1.5	16.7max.
2512	8kpcs/Reel	254.0±2.0	100.0±0.5	13.5±0.5	20(Min.)	2.0±0.5	13.8±1.5	20.0max.
	16kpcs/Reel	330.0±2.0	100.0±1.0	13.5±0.5	20(Min.)	2.0±0.5	13.8±1.5	20.0max.

unit:mm

10. Storage & Handling

... Products are recommended to be used up within one year as ensured shelf life.

Check solder ability in case shelf life extension is needed.

... To store products with following condition:

Temperature:5 to 40°C ; Humidity: 20 to 70% relative humidity.

Precaution for use :

The AEC-Q200 series resistors is mainly used on general automotive equipment without safety considerations.

Please contact our company in advanced if you intend to use resistor for designing the equipment which may

damage itself and the safety of third party. If necessary, please consider to add the protect circuit in devising

process and obtaining fully safety evaluation. The contents of the acknowledgment is only used for our parent

company, marketing subsidiaries and official marketing agents who purchase our products. Not applicable for the

other nonofficial channels.